

IN THE CLAIMS

Please amend Claims ~~1~~, ~~18~~ and ~~49~~ to read as follows (a version marked to show the changes is submitted herewith):

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1. (Five Times Amended) A matrix substrate having plural switching elements provided in matrix corresponding to intersecting points of scanning lines and signal lines, plural picture element electrodes connected to the switching elements, and horizontal circuits and vertical circuits for inputting the signals to the switching elements, the matrix substrate comprising:

an input terminal for inputting digital video data;

a circuit for changing an initial sequencing order of the digital video data inputted through said input terminal into a different sequencing order, and for outputting the digital video data in the different sequencing order;

a horizontal scanning circuit for sampling the digital video data in the initial sequencing order;

a latch circuit for memorizing the data synchronously with output from the horizontal scanning circuit;

a D/A converter for converting an output from the latch circuit into analog signals;

plural signal transfer switches provided between the D/A converter and the signal lines;

a selection circuit for selecting at least one of the signal transfer switches to output analog signals in the same order as the different sequencing order of the digital video data;

circuitry which inputs signal-polarity inverting signals together with the picture data, and which inverts the polarity of the analog signal from the D/A converter; and

a buffer disposed between said D/A converter and said selection circuit, which stores the analog signal of inverted polarity from the D/A converter,

wherein a number M of said D/A converters is less than a number N of said switching elements arranged in a horizontal direction, and analog signals are sequentially inputted from particular ones of said M D/A converters to N/M plural switching elements arranged in a horizontal direction.

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18. (Five Times Amended) A liquid crystal device comprising a matrix substrate having plural switching elements provided in matrix corresponding to intersecting points of scanning lines and signal lines, plural picture element electrodes connected to the switching elements, and horizontal circuits and vertical circuits for inputting the signals to the switching elements; a counter substrate opposing to the matrix substrate; and a liquid crystal material placed between the matrix substrate and the counter substrate, the matrix substrate comprising:

an input terminal for inputting digital video data;

a circuit for changing an initial sequencing order of the digital video data inputted through said input terminal into a different sequencing order, and for outputting the digital video data in the different sequencing order;

a horizontal scanning circuit for sampling the digital video data in the initial order;

a latch circuit for memorizing the data synchronously with output from the horizontal scanning circuit;

a D/A converter for converting the output from the latch circuit into analog signals;

plural signal transfer switches provided between D/A converter and the signal lines;

a buffer disposed between said D/A converter and said plural signal transfer switches, which stores the analog signal of inverted polarity from the D/A converter;

*SB*  
*cont*  
a selection circuit for selecting at least one of the signal transfer switches to output analog signals in the same order as the different sequencing order of the digital video data; and

means for inputting signal-polarity inverting signals together with the picture data, and for inverting the polarity of the analog output of the D/A converter,

wherein a number M of said D/A converters is less than a number N of said switching elements arranged in a horizontal direction, and analog signals are sequentially inputted from particular ones of said M D/A converters to N/M plural switching elements arranged in a horizontal direction.

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49. (Amended) A horizontal circuit comprising:

*SB*  
*cont*  
an input terminal for inputting digital video data;

a sequencing circuit for changing an initial sequencing order of the digital video data inputted through said input terminal into a different sequencing order, and for outputting the digital video data in the different sequencing order;